15432V1

5.000V

5VdcD 0V

R20V

0V

\_

U1

0

21.24uV 0

Vsin

-612.2mV

RgC121.24uV

C2160k U2A

C535.18uV3.3n

46.52uV

R6

-612.2mV

68k C6

3.3n

-5.000V

U5C

TL084

5

4

3

2

1

D

FREQ = 250

22n

22n

49.11uV RgINA128 Vsin+0V

+

Ref

R40V 0V

18.66uV

64.57uV

16k

34.98uV

C 0

32.39uV

C

0V

0

B 0D1D1N4148

3 + VAMPL = 10mv VOFF = 0 AC = 20mV

Rg240

-108.8uV

32.80uV

R5

U3B 5 + 2

- 68k TL084OUT 1 R1

U4C +

OUT 7

10 + 6

- OUT 8

Vout

160k

TL084 R8 V

0

9 - TL084 0

0V

24k

R12

R3R7 51k

51k

R11

1k

0V

1k

0 0V 0 -5.000V

V2

R16

64.57uV

5Vdc

U15C 0V

100k

B

0V C70

-2.567mV

TL084

R32 10k

V

0.01uF

-14.12uV

0

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100k

R17 5.000V -14.12uV

-33.19uV 100k

-15.55uV

-5.000V

10 -46.39uV

+ D6

10

+ U6C

-29.71uV

10

+ OUT 8

9

- C10

10uF

TL084

-46.39uV

-3.390V

Dbreak

R13

OUT 8

OUT 8

V

9

- 9

- 0V 100k D2R15

D1N4148

2.016uV

100k

Title Title

Senior Senior Senior design design design project project project Alexander Alexander Alexander Volkov Volkov Volkov

Size Size Document Document Number Number Rev Rev A A A

EMG EMG EMG signal signal signal acquisition acquisition acquisition 1b 1b 1b

Date: Date: Friday, Friday, Friday, November November November 22, 22, 22, 2019 2019 2019

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